FOUND: 6,172,860

Fig. 1 PRIOR ART

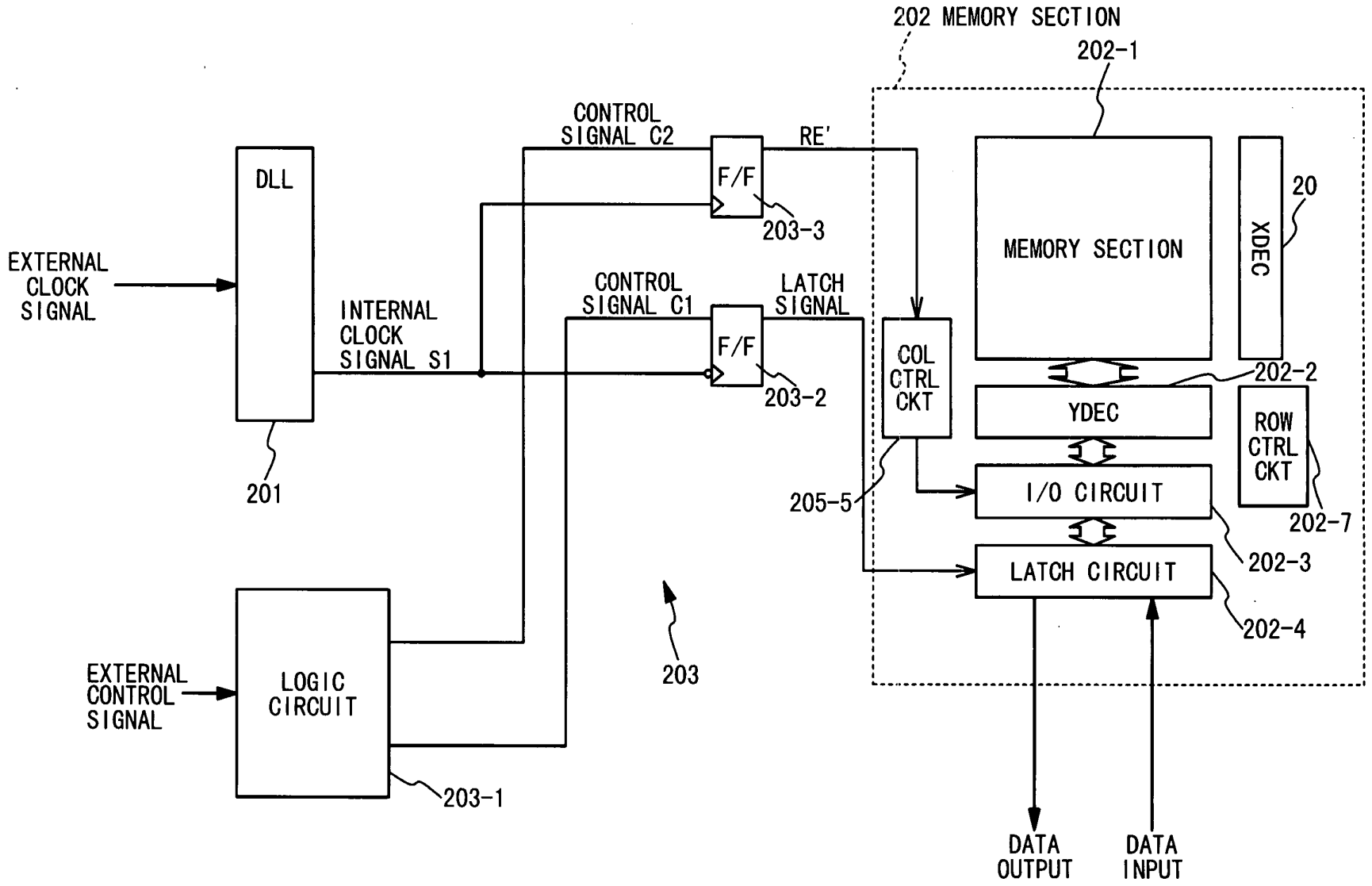


Fig. 2A
PRIOR ART

Fig. 2B
PRIOR ART

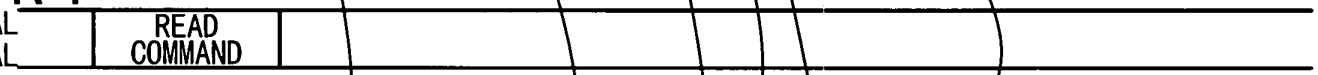


Fig. 2C
PRIOR ART

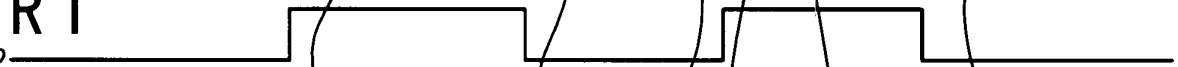


Fig. 2D
PRIOR ART

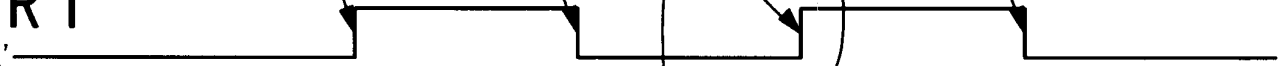


Fig. 2E
PRIOR ART

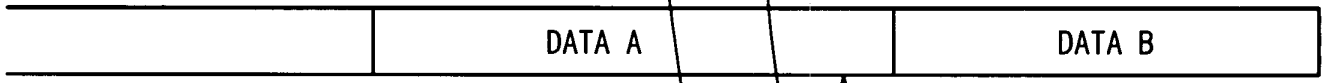


Fig. 2F
PRIOR ART

Fig. 2G
PRIOR ART

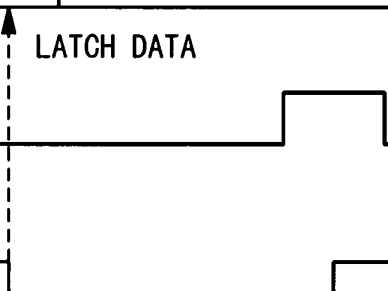


Fig. 3 PRIOR ART

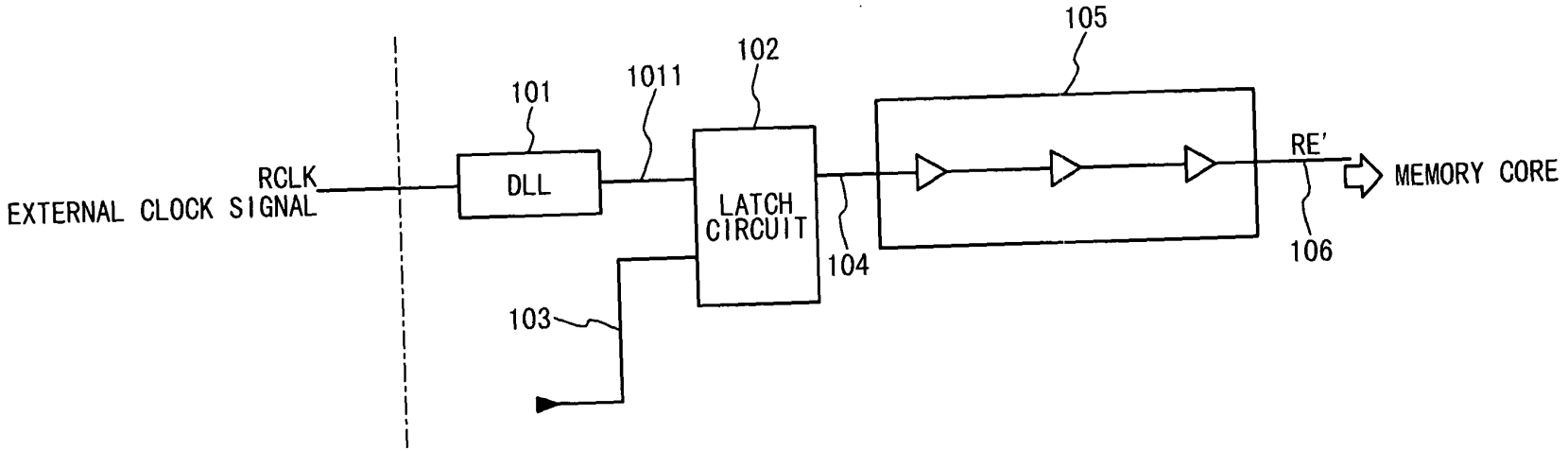


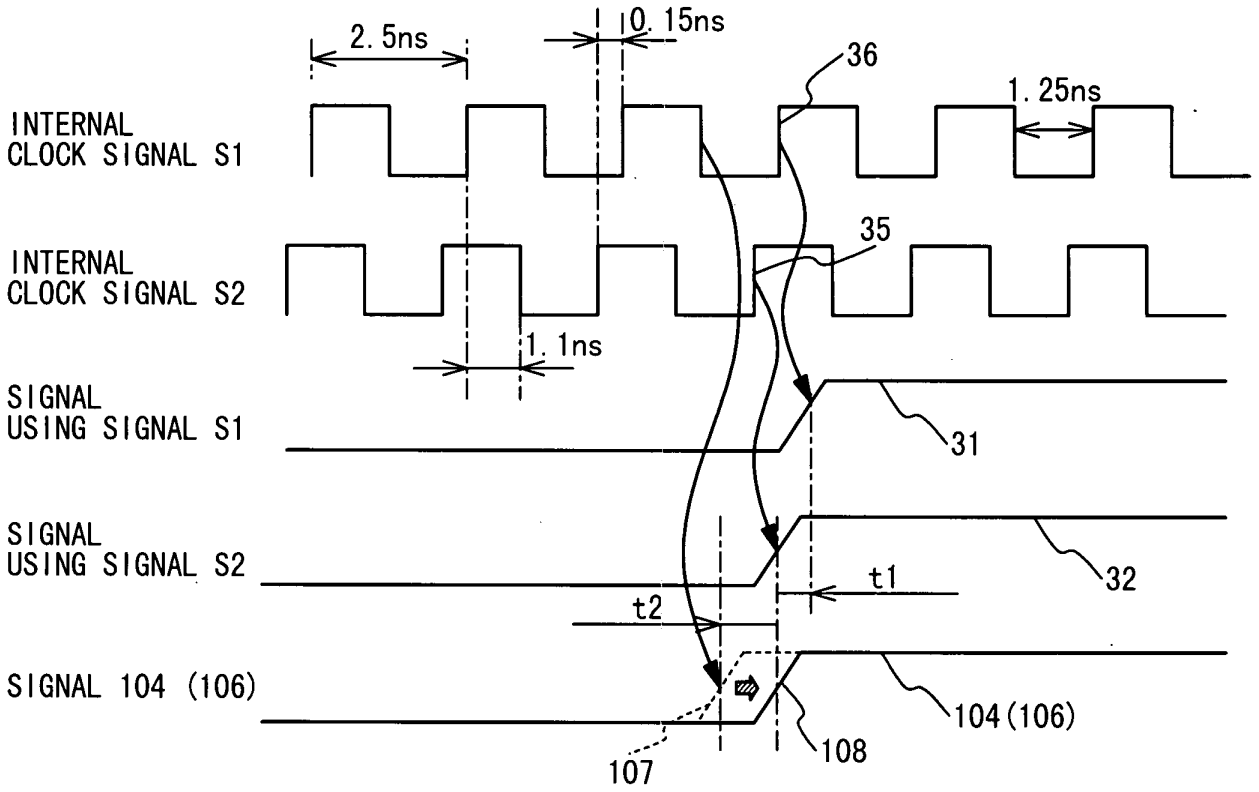
Fig. 4A

Fig. 4B

Fig. 4C
PRIOR ART

Fig. 4D

Fig. 4E
PRIOR ART



FOHOHO" 6ZT92860

Fig. 5A

Fig. 5B

Fig. 5C
PRIOR ART

Fig. 5D

Fig. 5E
PRIOR ART

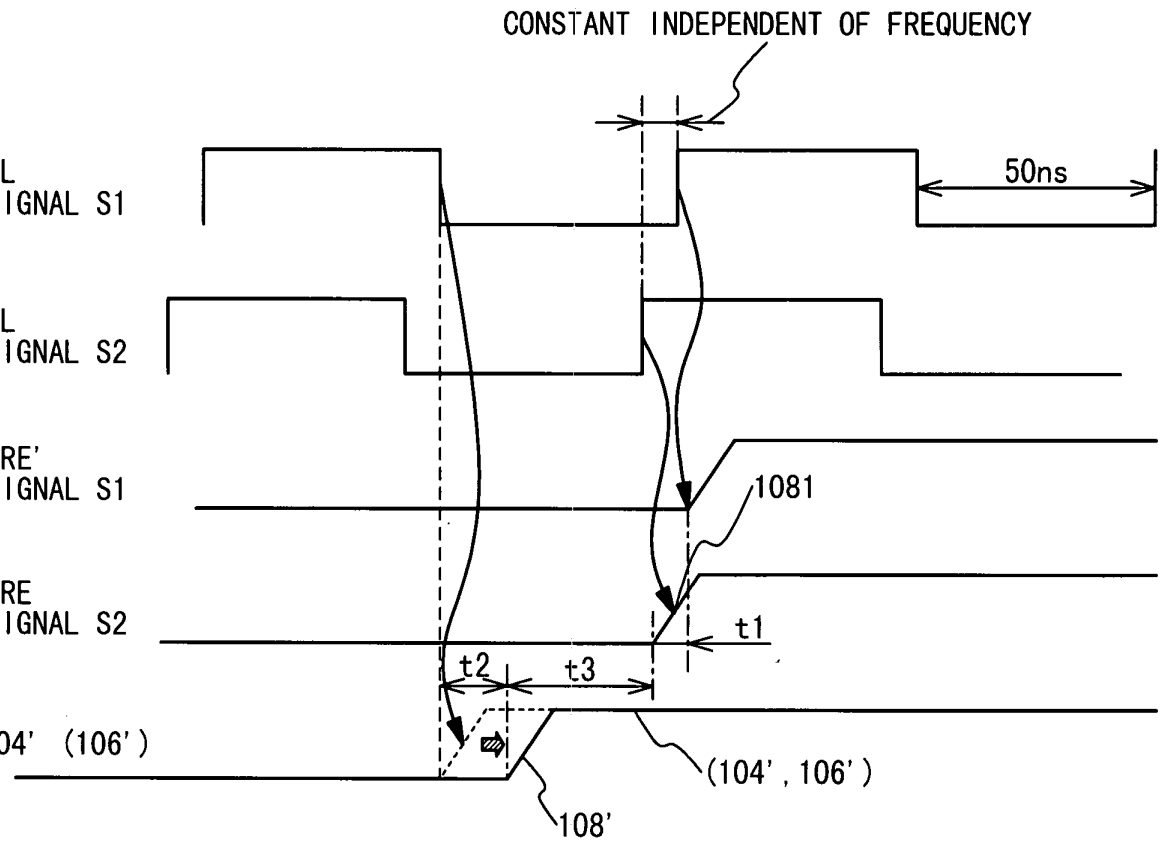


Fig. 6A
PRIOR ART

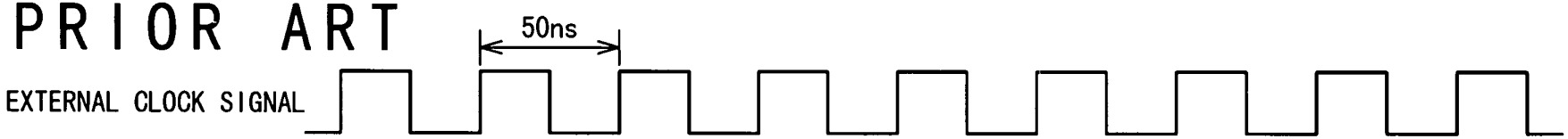


Fig. 6B
PRIOR ART

Fig. 6C
PRIOR ART

Fig. 6D
PRIOR ART

Fig. 6E
PRIOR ART

Fig. 6F
PRIOR ART

Fig. 6G
PRIOR ART

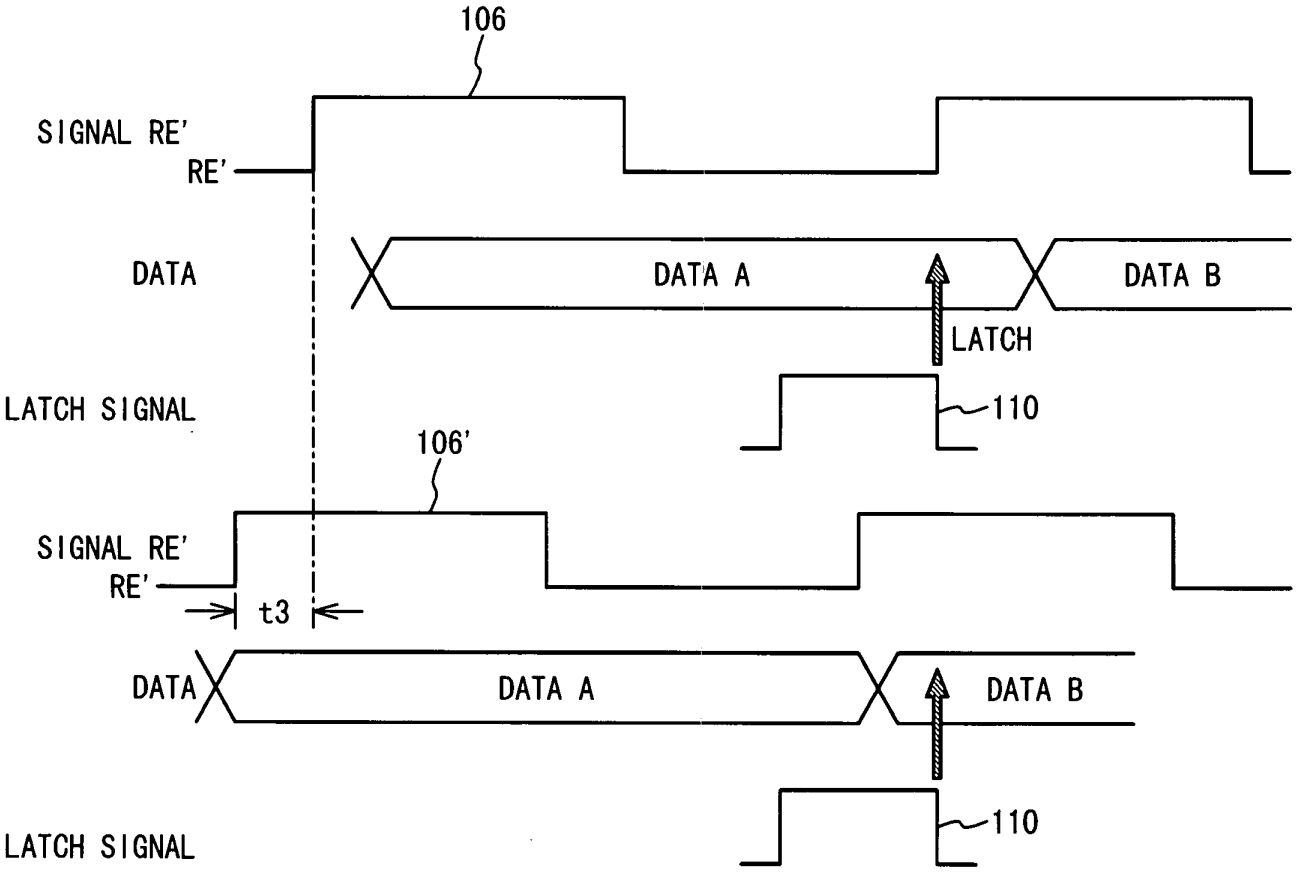


Fig. 7A

Fig. 7B

Fig. 7C

Fig. 7D
PRIOR ART

Fig. 7E
PRIOR ART

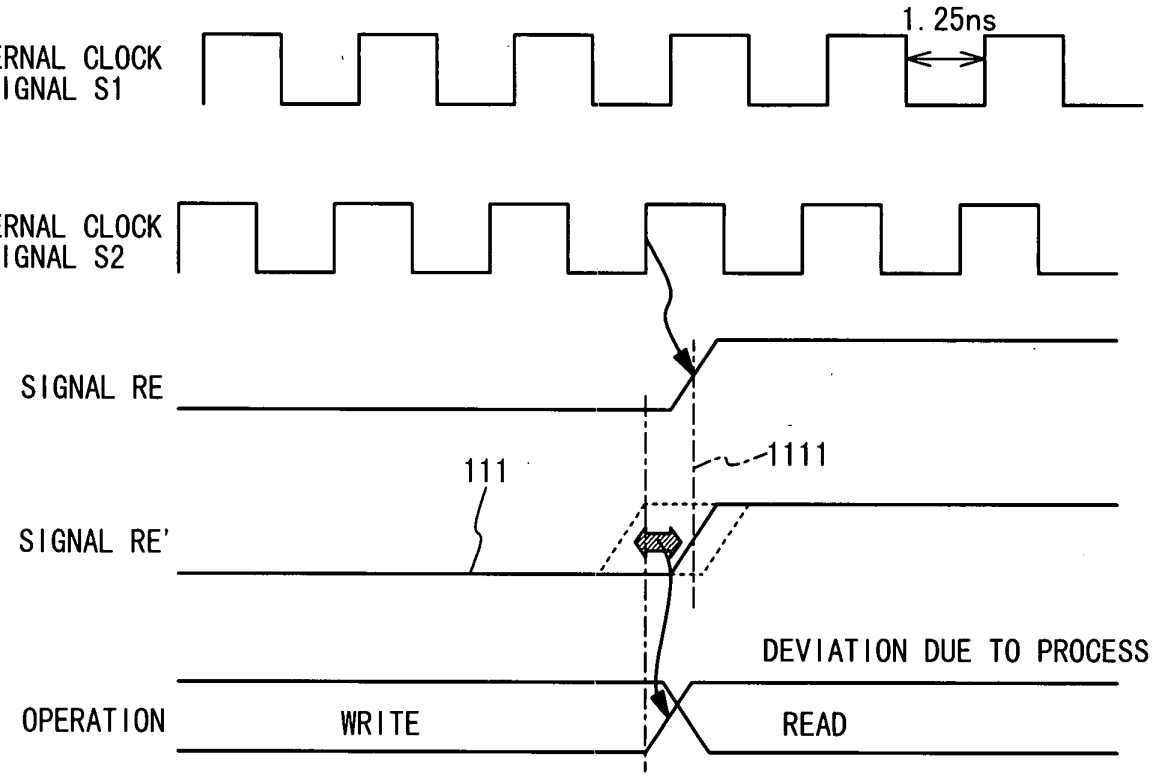


Fig. 8A

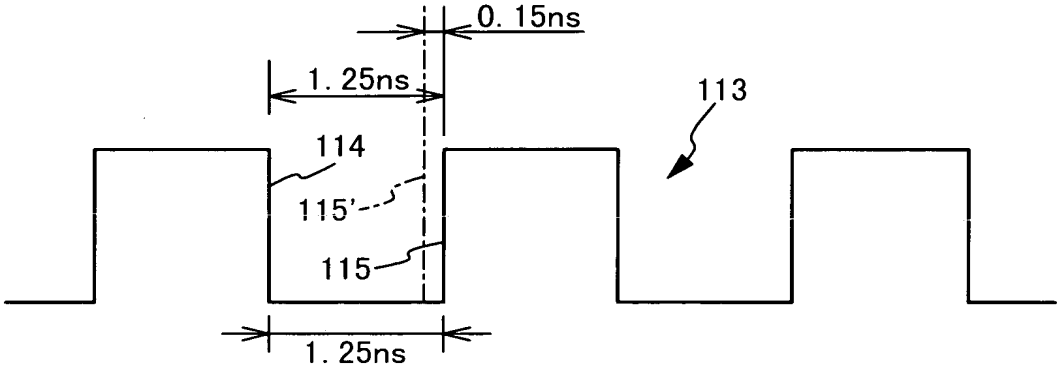


Fig. 8B PRIOR ART

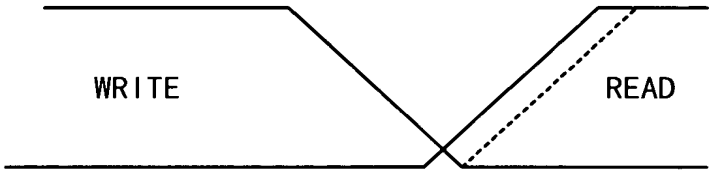


Fig. 8C PRIOR ART

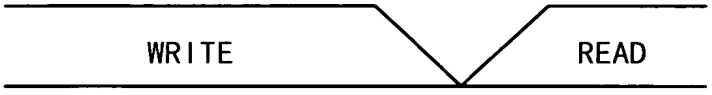


Fig. 9 PRIOR ART

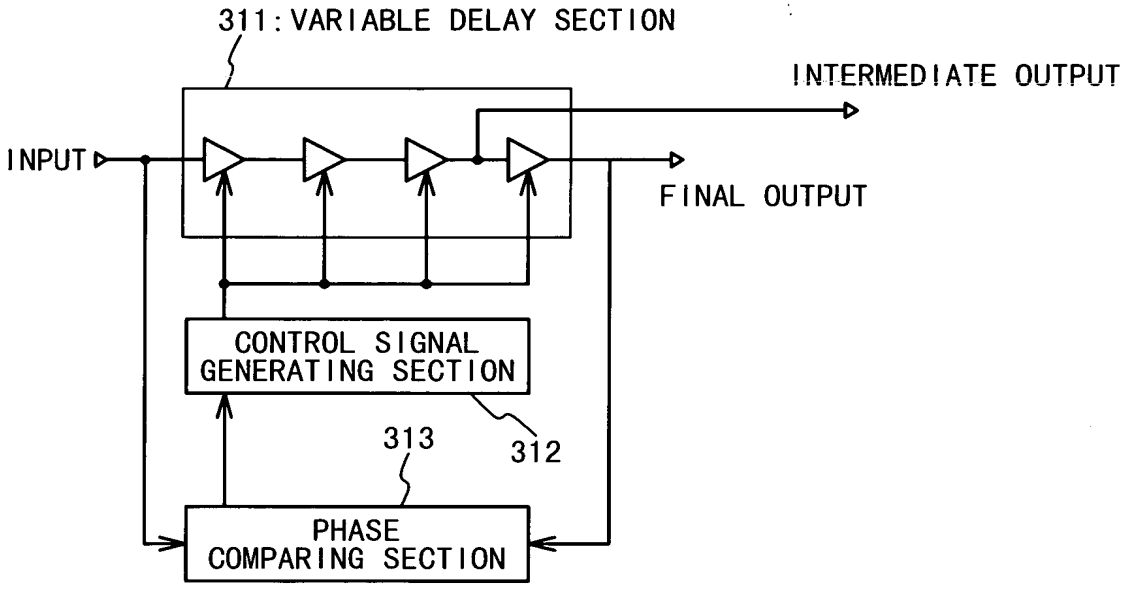


Fig. 10

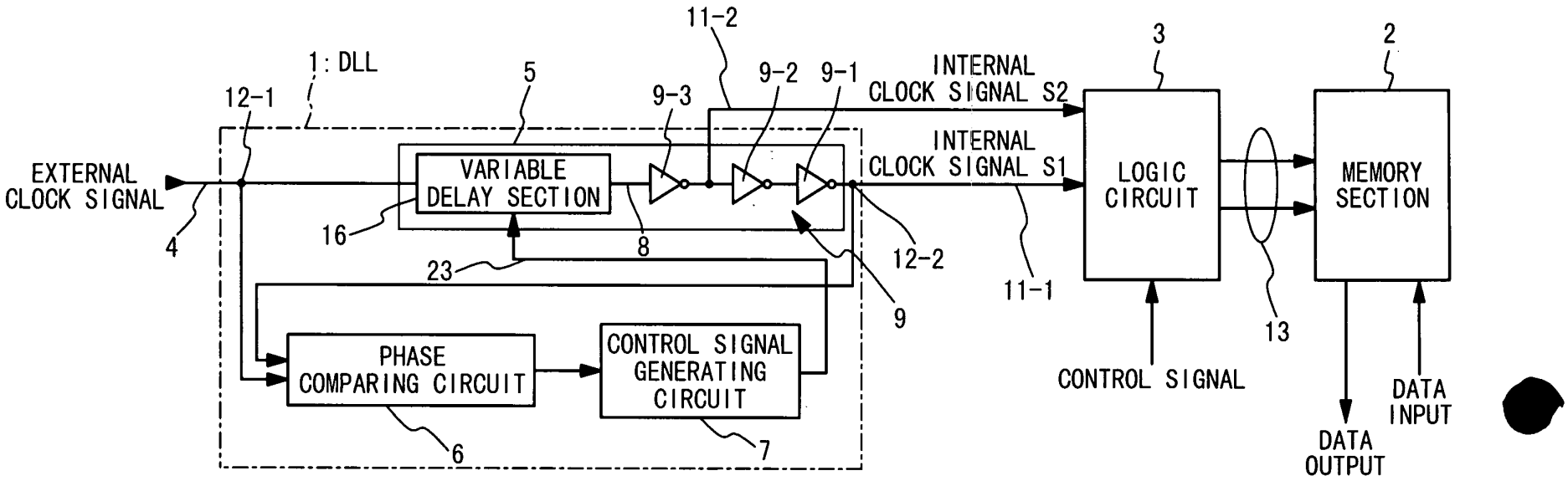

1: DLL
12-1
5
11-2
9-3
9-2
9-1
INTERNAL CLOCK SIGNAL S2
INTERNAL CLOCK SIGNAL S1
3
2
EXTERNAL CLOCK SIGNAL
4
VARIABLE DELAY SECTION
16
8
23
12-2
11-1
9
LOGIC CIRCUIT
MEMORY SECTION
13
PHASE COMPARING CIRCUIT
CONTROL SIGNAL GENERATING CIRCUIT
6
7
CONTROL SIGNAL
DATA INPUT
DATA OUTPUT

Fig. 11

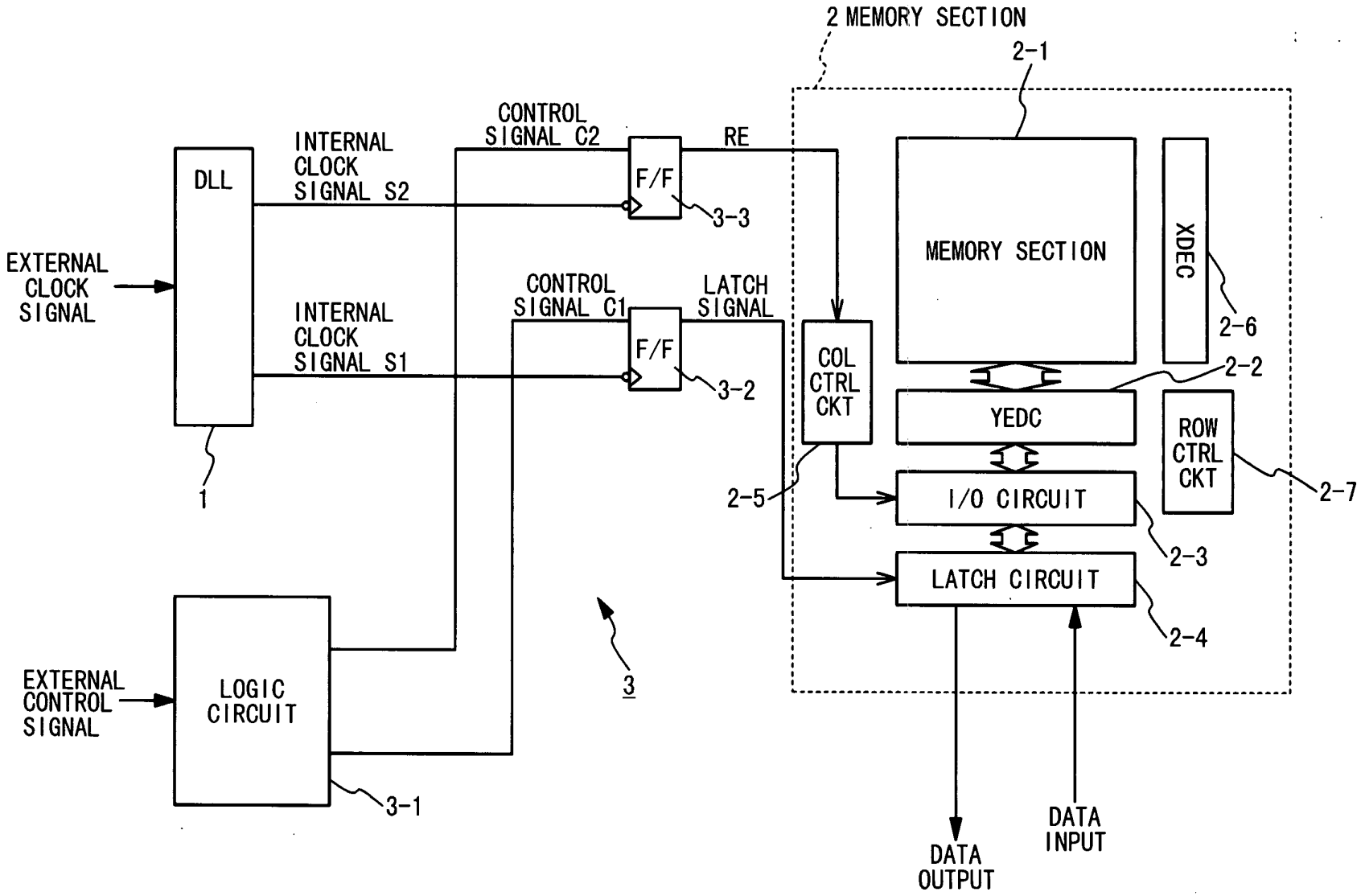

2 MEMORY SECTION
2-1
DLL
INTERNAL CLOCK SIGNAL S2
CONTROL SIGNAL C2
F/F
RE
3-3
EXTERNAL CLOCK SIGNAL
INTERNAL CLOCK SIGNAL S1
CONTROL SIGNAL C1
LATCH SIGNAL
F/F
3-2
1
COL CTRL CKT
MEMORY SECTION
XDEC
2-6
2-2
YEDC
ROW CTRL CKT
2-7
2-5
I/O CIRCUIT
2-3
LATCH CIRCUIT
2-4
EXTERNAL CONTROL SIGNAL
LOGIC CIRCUIT
3
3-1
DATA OUTPUT
DATA INPUT

Fig. 12A

Fig. 12B

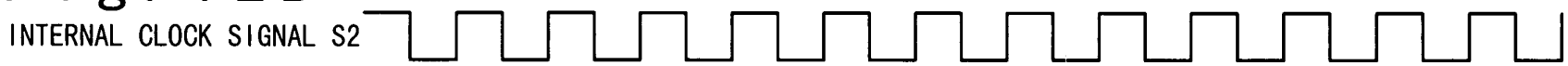


Fig. 12C

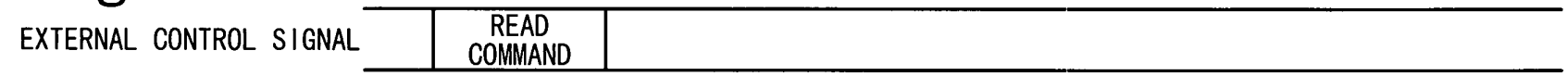


Fig. 12D

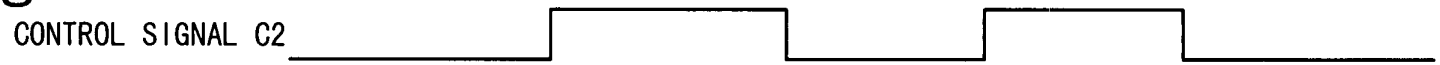


Fig. 12E

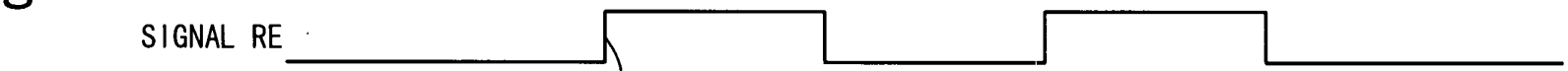


Fig. 12F

Fig. 12G

Fig. 12H